Attorney Docket No. 001542 U.S. Patent Appl. No. 09/715,081 Page 2

## IN THE CLAIMS:

Claim 1 (Currently amended): A manufacturing system for manufacturing printed wiring boards of plural types, said printed wiring boards scheduled to be manufactured are laid out on at least one predetermined manufacturing block, comprising:

a schedule data storage unit storing manufacturing schedule data including printed wiring board data, each type of the printed wiring boards and a number of each of the printed wiring boards scheduled to be manufactured;

a detecting unit detecting a plurality of fractional printed wiring boards which should be laid out to a format of a single predetermined manufacturing block together with a plurality of printed wiring boards having a different type within the printed wiring boards scheduled to be manufactured based on the manufacturing schedule data stored in said schedule data storage unit, printed wiring board data, and predetermined manufacturing block data;

a condition data storage unit storing manufacturing condition data for laying out printed wiring boards of the plural types on a format of a single predetermined manufacturing block;

a grouping unit grouping each of the fractional printed wiring boards detected by said detecting unit into any of a number of groups according to the manufacturing condition data stored in said condition data storage unit; and

a determining unit determining, per group, layout to the at least one predetermined manufacturing block of the fractional printed wiring board.

Claim 2 (previously presented): A manufacturing system according to claim 1, wherein

said detecting unit, if the number of printed wiring boards of a certain type scheduled to be

manufactured cannot be divided completely by the maximum number of the printed wiring boards

which can be laid out in a single predetermined manufacturing block, detects each printed wiring

board corresponding to the number smaller than the maximum number or each printed wiring

board corresponding to the remainder of the division as the fractional printed wiring board.

Claim 3 (previously presented): A manufacturing system according to claim 1, wherein the

manufacturing condition data is data produced by combining manufacturing request person's

condition and manufacturer's condition.

Claim 4 (previously presented): A manufacturing system according to claim 3, wherein the

manufacturing request person's condition is shipment date.

Claim 5 (previously presented): A manufacturing system according to claim 3, wherein the

manufacturer's condition is number of layers of the printed wiring boards.

Claim 6 (previously presented): A manufacturing system according to claim 4, wherein the

manufacturer's condition is number of layers of the printed wiring boards.

Claim 7 (previously presented): A manufacturing system according to claim 1 further

comprising:

a CAD data creating unit creating CAD data corresponding to a combination determined by

said determining unit; and

a CAD data converting unit creating CAM data or CAT data corresponding to CAD data

created by said CAD data creating unit.

Claim 8 (previously presented): A manufacturing system according to claim 7 further

comprising:

manufacturing unit group carrying out manufacturing process for the printed wiring board

using the CAM data or CAT data created by said CAD data converting unit.

Claim 9 (previously presented): A manufacturing method for manufacturing printed

wiring boards of plural types, said printed wiring boards scheduled to be manufactured are laid out

to at least one predetermined manufacturing block, comprising:

reading manufacturing schedule data including printed wiring board data, each of the plural

types of the printed wiring boards and a number of each of the printed wiring boards scheduled to

be manufactured;

detecting a plurality of fractional printed wiring boards which should be laid out to a format

of a single predetermined manufacturing block together with a plurality of printed wiring boards

Attorney Docket No. 001542 U.S. Patent Appl. No. 09/715,081

Page 5

having a different type within the printed wiring boards scheduled to be manufactured based on the

manufacturing schedule data;

reading a manufacturing condition data for laying out printed wiring boards of the plural

types on a format of a single predetermined manufacturing block;

grouping each of the detected fractional printed wiring boards into any of a number of

groups according to the manufacturing condition data; and

determining, per group, layout to the at least one predetermined manufacturing block of the

fractional printed wiring board.

Claim 10 (previously presented): A computer-readable recording medium for recording a

computer program for making a computer to carry out processes for manufacturing printed wiring

boards of plural types, said printed wiring boards scheduled to be manufactured are laid out to at

least one predetermined manufacturing block, the program comprising:

reading manufacturing schedule data including printed wiring board data, each of the plural

types of the printed wiring boards and a number of each of the printed wiring boards scheduled to

be manufactured;

detecting a plurality of fractional printed wiring boards which should be laid out to a single

predetermined manufacturing block together with a plurality of printed wiring boards having a

different type within the printed wiring boards scheduled to be manufactured based on the multiple

manufacturing schedule data;

reading manufacturing condition data for laying out printed wiring boards of the plural

Attorney Docket No. 001542 U.S. Patent Appl. No. 09/715,081 Page 6

types to a format of a single predetermined manufacturing block;

grouping each of the detected fractional printed wiring boards into any of a number of groups according to the manufacturing condition data; and

determining, per group, layout to the at least one predetermined manufacturing block of the fractional printed wiring board.